



Cambridge Assessment International Education
Cambridge International General Certificate of Secondary Education

COMPUTER SCIENCE

0478/11

Paper 1

May/June 2018

MARK SCHEME

Maximum Mark: 75

Published

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the May/June 2018 series for most Cambridge IGCSE™, Cambridge International A and AS Level and Cambridge Pre-U components, and some Cambridge O Level components.

IGCSE™ is a registered trademark.

This syllabus is approved for use in England, Wales and Northern Ireland as a Cambridge International Level 1/Level 2 Certificate.

This document consists of **12** printed pages.

PUBLISHED**Generic Marking Principles**

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

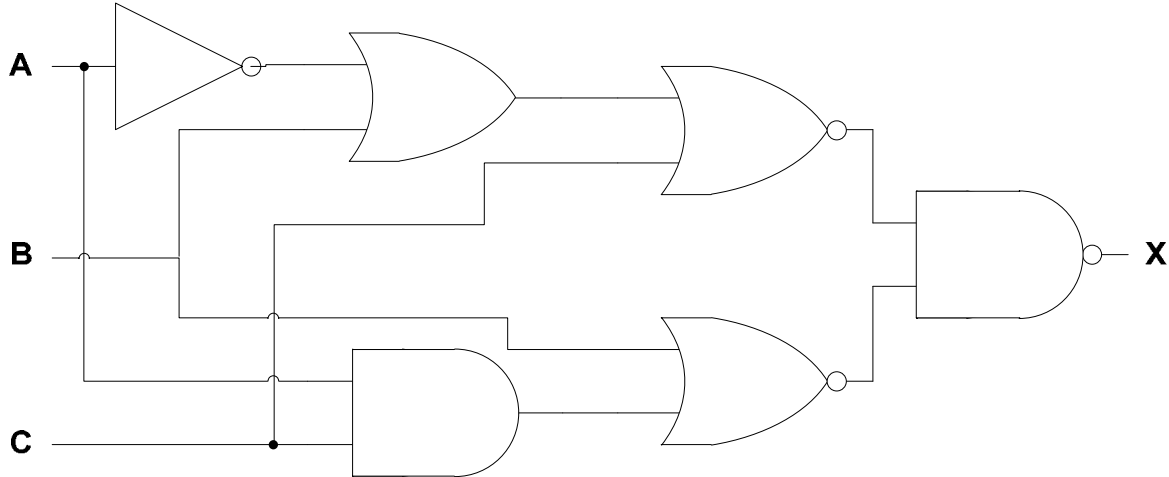
Question	Answer	Marks
1	1 mark for each correct answer, in the given order: <ul style="list-style-type: none"> – analogue – digital – denary – 10 – binary – 2 	6

Question	Answer	Marks
2	1 mark for each correct conversion: <ul style="list-style-type: none"> – 42 – 257 – 542 	3

Question	Answer	Marks
3	1 mark for correct register, 3 marks for reason: <ul style="list-style-type: none"> – Register C <p>Any three from:</p> <ul style="list-style-type: none"> – Count the number of 1/0 bits (in each byte/register) – Two bytes/registers have an odd number of 1/0 bits // Two use odd parity – Odd parity must be the parity used – One byte/register has an even number of 1/0 bits // One uses even parity – One with an even number of one bits/even parity is incorrect // Register C should have odd parity 	4

Question	Answer	Marks
4(a)	1 mark for each correct answer: Lossy (compression) Lossless (compression)	2
4(b)	1 mark for correct compression, 3 marks for description: – Lossless (compression) Any three from: – The file can be restored/decompressed to the exact same state it was before compression/ to original – (It is a computer program so) no data can be lost // Lossy would remove data – Will not run correctly (with any other compression) – (Lossless) will give repeating words/sections of word a value// RLE is used // Other valid examples of methods of lossless compression – Value is recorded in an index	4

Question	Answer	Marks														
5	<p>1 mark for each correct line, up to a maximum of 5 marks:</p> <table border="0"> <thead> <tr> <th data-bbox="338 284 936 320">Component</th> <th data-bbox="936 284 1541 320">Description</th> </tr> </thead> <tbody> <tr> <td data-bbox="338 320 663 424"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Immediate access store (IAS)</div> </td> <td data-bbox="936 320 1541 424"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data and instructions when they are loaded from main memory and are waiting to be processed.</div> </td> </tr> <tr> <td data-bbox="338 504 663 592"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Register</div> </td> <td data-bbox="936 504 1541 592"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data temporarily that is currently being used in a calculation.</div> </td> </tr> <tr> <td data-bbox="338 671 663 759"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Control unit (CU)</div> </td> <td data-bbox="936 671 1541 759"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data or instructions temporarily when they are being processed.</div> </td> </tr> <tr> <td data-bbox="338 839 663 927"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Accumulator (ACC)</div> </td> <td data-bbox="936 839 1541 927"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Manages the flow of data and interaction between the components of the processor.</div> </td> </tr> <tr> <td data-bbox="338 1007 663 1094"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Arithmetic logic unit (ALU)</div> </td> <td data-bbox="936 1007 1541 1094"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Carries out the calculations on data.</div> </td> </tr> <tr> <td data-bbox="338 1174 663 1262"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Bus</div> </td> <td data-bbox="936 1174 1541 1262"> <div style="border: 1px solid black; padding: 5px; width: fit-content;">Pathway for transmitting data and instructions.</div> </td> </tr> </tbody> </table>	Component	Description	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Immediate access store (IAS)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data and instructions when they are loaded from main memory and are waiting to be processed.</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Register</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data temporarily that is currently being used in a calculation.</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Control unit (CU)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data or instructions temporarily when they are being processed.</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Accumulator (ACC)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Manages the flow of data and interaction between the components of the processor.</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Arithmetic logic unit (ALU)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Carries out the calculations on data.</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Bus</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Pathway for transmitting data and instructions.</div>	5
Component	Description															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Immediate access store (IAS)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data and instructions when they are loaded from main memory and are waiting to be processed.</div>															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Register</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data temporarily that is currently being used in a calculation.</div>															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Control unit (CU)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Holds data or instructions temporarily when they are being processed.</div>															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Accumulator (ACC)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Manages the flow of data and interaction between the components of the processor.</div>															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Arithmetic logic unit (ALU)</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Carries out the calculations on data.</div>															
<div style="border: 1px solid black; padding: 5px; width: fit-content;">Bus</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content;">Pathway for transmitting data and instructions.</div>															

Question	Answer	Marks
6(a)	<p>1 mark for each correct logic gate (with the correct direction of input(s))</p>  <p>The diagram shows a logic circuit with three inputs: A, B, and C. Input A is connected to an inverter. The output of the inverter is connected to the top input of an OR gate. Input B is connected to the bottom input of this OR gate. The output of this OR gate is connected to the top input of a second OR gate. Input C is connected to the bottom input of this second OR gate. The output of the second OR gate is connected to the top input of an AND gate. Input B is also connected to the bottom input of this AND gate. The output of this AND gate is connected to the top input of a final AND gate. Input C is also connected to the bottom input of this final AND gate. The output of the final AND gate is labeled X.</p>	6

Question	Answer	Marks																																													
6(b)	<p>4 marks for 8 correct outputs 3 marks for 6 or 7 correct outputs 2 marks for 4 or 5 correct outputs 1 mark for 2 or 3 correct outputs</p> <table border="1" data-bbox="342 384 1361 839"><thead><tr><th data-bbox="342 384 421 435">A</th><th data-bbox="421 384 499 435">B</th><th data-bbox="499 384 577 435">C</th><th data-bbox="577 384 1285 435">Working space</th><th data-bbox="1285 384 1361 435">X</th></tr></thead><tbody><tr><td data-bbox="342 435 421 486">0</td><td data-bbox="421 435 499 486">0</td><td data-bbox="499 435 577 486">0</td><td data-bbox="577 435 1285 486"></td><td data-bbox="1285 435 1361 486">1</td></tr><tr><td data-bbox="342 486 421 537">0</td><td data-bbox="421 486 499 537">0</td><td data-bbox="499 486 577 537">1</td><td data-bbox="577 486 1285 537"></td><td data-bbox="1285 486 1361 537">1</td></tr><tr><td data-bbox="342 537 421 588">0</td><td data-bbox="421 537 499 588">1</td><td data-bbox="499 537 577 588">0</td><td data-bbox="577 537 1285 588"></td><td data-bbox="1285 537 1361 588">1</td></tr><tr><td data-bbox="342 588 421 639">0</td><td data-bbox="421 588 499 639">1</td><td data-bbox="499 588 577 639">1</td><td data-bbox="577 588 1285 639"></td><td data-bbox="1285 588 1361 639">1</td></tr><tr><td data-bbox="342 639 421 691">1</td><td data-bbox="421 639 499 691">0</td><td data-bbox="499 639 577 691">0</td><td data-bbox="577 639 1285 691"></td><td data-bbox="1285 639 1361 691">0</td></tr><tr><td data-bbox="342 691 421 742">1</td><td data-bbox="421 691 499 742">0</td><td data-bbox="499 691 577 742">1</td><td data-bbox="577 691 1285 742"></td><td data-bbox="1285 691 1361 742">1</td></tr><tr><td data-bbox="342 742 421 793">1</td><td data-bbox="421 742 499 793">1</td><td data-bbox="499 742 577 793">0</td><td data-bbox="577 742 1285 793"></td><td data-bbox="1285 742 1361 793">1</td></tr><tr><td data-bbox="342 793 421 844">1</td><td data-bbox="421 793 499 844">1</td><td data-bbox="499 793 577 844">1</td><td data-bbox="577 793 1285 844"></td><td data-bbox="1285 793 1361 844">1</td></tr></tbody></table>	A	B	C	Working space	X	0	0	0		1	0	0	1		1	0	1	0		1	0	1	1		1	1	0	0		0	1	0	1		1	1	1	0		1	1	1	1		1	4
A	B	C	Working space	X																																											
0	0	0		1																																											
0	0	1		1																																											
0	1	0		1																																											
0	1	1		1																																											
1	0	0		0																																											
1	0	1		1																																											
1	1	0		1																																											
1	1	1		1																																											

Question	Answer	Marks
7	<p>Compiler</p> <p>Any three from:</p> <ul style="list-style-type: none"> – Translates high-level language into machine code/low level language – Translates (the source code) all in one go/all at once – Produces an executable file – Produces an error report <p>Interpreter</p> <p>Any three from:</p> <ul style="list-style-type: none"> – Translates high-level language into machine code/low level language – Translates (the source code) line by line/statement by statement – Stops if it finds an error – Will only continue when error is fixed 	6

Question	Answer	Marks
8(a)	<p>Any four from:</p> <ul style="list-style-type: none"> – Shines light / (red) laser at barcode – Light is called an illuminator – Light is reflected back // White lines reflect light // Black lines reflect less light/absorbs light – Sensors / photoelectric cells detect the light – Different reflections / bars will give different binary values / digital values // pattern converted to digital values – A microprocessor interprets the data 	4
8(b)	<p>Any three from:</p> <ul style="list-style-type: none"> – barcode identifies a (unique) product – barcode can be used to look up product (in a database) – data about stock levels can be stored on a system – stock can be automatically deducted from the system – can check stock is below a certain level // check stock level – automatic re-order // Alerts when stock is low – automatically update new stock level – to locate if an item of stock is available in another location 	3

Question	Answer	Marks
8(c)	Any four from: <ul style="list-style-type: none"> – (Infrared) rays are sent across screen (from the edges) – Has sensors around edge // Sensors capture beams – (Infrared) rays form a grid across the screen – (Infrared) ray is broken (by a finger blocking a beam) – Calculation is made (on where beam is broken) to locate the 'touch' // Co-ordinates are used to locate the touch 	4
8(d)	Secondary Storage – any two from: <ul style="list-style-type: none"> – Not directly accessed by the CPU – Non-volatile storage – Secondary is internal to the computer/device – An example of secondary storage would be HDD/SSD Off-line storage – any two from: <ul style="list-style-type: none"> – Non-volatile storage – Off-line storage is storage that is removable from a computer/device // not internal // portable – An example of off-line storage would be CD/DVD/USB stick/SD card/magnetic tape/ external HDD/SSD 	4

Question	Answer	Marks
9	Any six from: <ul style="list-style-type: none"> – Suitable biometric device, such as fingerprint scanner/retina/eye/iris scanner/face recognition/voice recognition/palm scanner // description of use e.g. use fingerprint on device – Sensor (in biometric device) captures/takes data/readings (of user) – Data/readings are converted from analogue to digital (using ADC) – Data/reading sent to the microprocessor – Data/readings compared to stored values/data ... – ... if data/readings match user can enter – ... if data/readings do not match user is declined entry // user asked to try again ... – ... alert may be sent to security // alarm may sound 	6

Question	Answer	Marks
10(a)	Any four from: <ul style="list-style-type: none"> – Structure and presentation are defined using (mark-up) tags – Structure and presentation dictate the appearance of the website – Structure is used for layout – Example of structure – Presentation is used for formatting / style – Example of formatting – Separate file / CSS can be used for presentation content 	4
10(b)(i)	1 mark for each correct part <ul style="list-style-type: none"> – domain (name) – file name/webpage name 	2
10(b)(ii)	Any two from: <ul style="list-style-type: none"> – Hypertext Transfer Protocol Secure // it is the access protocol // It is a protocol – It means the website uses SSL/TLS – It means data sent (to and from the webserver) is encrypted 	2
10(c)	Any two from e.g. : <ul style="list-style-type: none"> – To store items that a customer has added to an online shopping basket – To store a customer's credit card details – To store log-in details – To track what product a customer browses // Track music preferences – Targeted advertising // making recommendations – Personalises/customises the experience – Shows who are new and returning customers – To speed up log-in times – To speed up/allow single click purchases – Improves the experience 	2

Question	Answer	Marks
10(d)	Any four from: <ul style="list-style-type: none">– Prevents direct access to the webserver // Sits between user and webserver– If an attack is launched it hits the proxy server instead // can be used to help prevent DDOS // help prevent hacking of webserver– Used to direct invalid traffic away from the webserver– Traffic is examined by the proxy server // Filters traffic– If traffic is valid the data from the webserver will be obtained by the user– If traffic is invalid the request to obtain data is declined– Can block requests from certain IP addresses	4