

Unit 8: Boolean Algebra
(A Level Only Content)

Marks: /36

Answer **all** the questions.

1. A half adder has the truth table shown below:

A	B	Sum	Carry
1	1	0	1
1	0	1	0
0	1	1	0
0	0	0	0

Draw a half adder using logic gates.

[3]

2(a). A cinema offers discounted tickets, but only under one of the following conditions:

- Customer is under 18 and has a student card.
- Customer is over 60 and has ID which proves this.

Let:

A be Customer is under 18

B be Customer has a student card

C be Customer is over 60

D be Customer has ID

Q be Discount ticket issued

Complete the Boolean expression below:

$Q \equiv$ [3]

(b). The cinema has a voucher which promises free popcorn when the voucher is produced whilst buying a soft drink or bottle of water.

Let:

E be Voucher is shown

F be Soft drink is bought

G be Bottle of water is bought

R be Free popcorn given.

This could be written as:

$$R \equiv (E \wedge F) \vee (E \wedge G)$$

(i) Complete the truth table below.

E	F	G	$(E \wedge F)$	$(E \wedge G)$	$(E \wedge F) \vee (E \wedge G)$
1	1	1			
1	1	0			
1	0	1			
1	0	0			
0	1	1			
0	1	0			
0	0	1			
0	0	0			

[4]

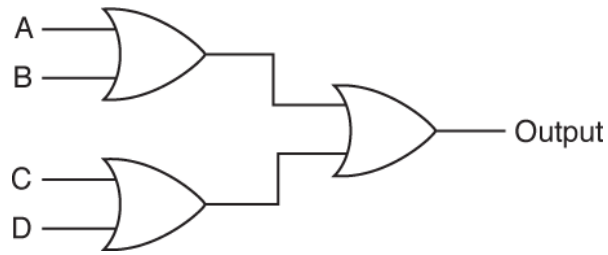
(ii) Simplify the expression

$$(E \wedge F) \vee (E \wedge G)$$

----- [2]

3.

A circuit contains the logic gates shown below.



(i) Complete the logic table below.

A	B	C	D	Output
1	1	1	1	
1	1	1	0	
1	1	0	1	
1	1	0	0	
1	0	1	1	
1	0	1	0	
1	0	0	1	
1	0	0	0	
0	1	1	1	
0	1	1	0	
0	1	0	1	
0	1	0	0	
0	0	1	1	
0	0	1	0	
0	0	0	1	
0	0	0	0	

[4]

(ii) Complete the Boolean expression below to represent the circuit.

----- \equiv Output

[2]

4(a). The component below is a D-Type, positive edge triggered, flip-flop.

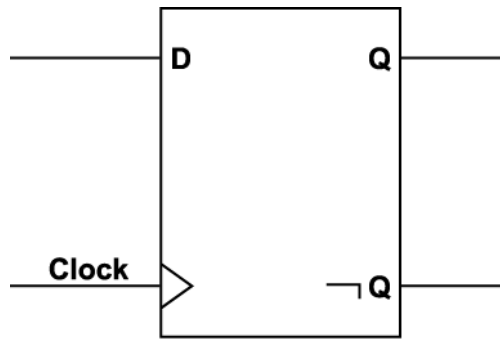
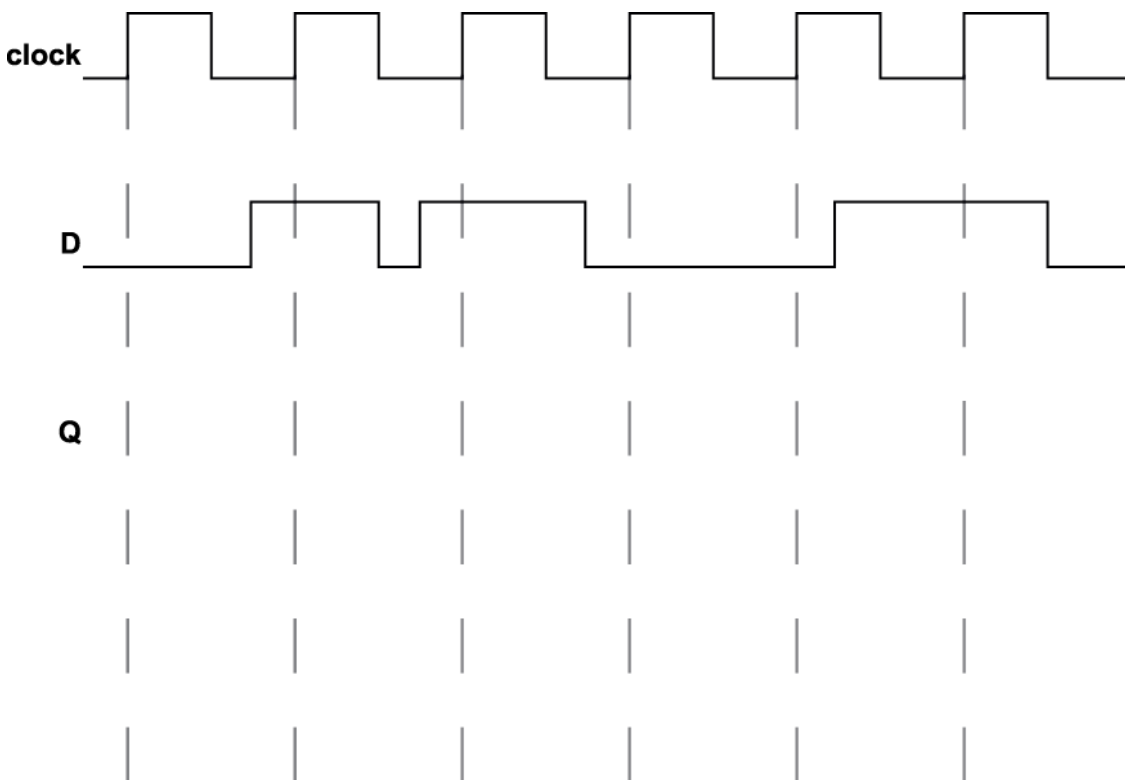


Fig. 10.2

State the purpose of a flip-flop.

----- [1]

(b). Draw the output of the flip-flop from Fig. 10.2 on the diagram below.



[3]

5. State the simplified versions of the following Boolean expressions:

(i) $\neg \neg A$

----- [1]

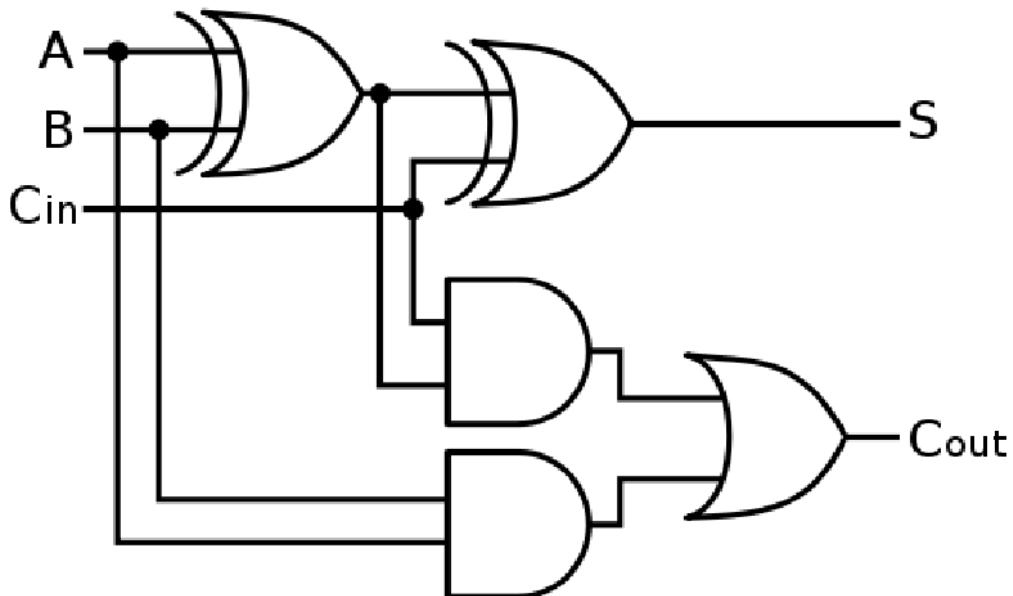
(ii) $(\neg A \wedge \neg B)$

----- [1]

(iii) $\neg(\neg A \wedge \neg B)$

----- [1]

6(a). A set of logic gates are connected as below.



(i) Complete the Truth Table below:

A	B	C _{in}	S	C _{out}
1	1	1		
1	1	0		
1	0	1		
1	0	0		
0	1	1		
0	1	0		
0	0	1		
0	0	0		

[4]

(ii) Explain what the circuit does. You should refer to A, B, C_{in}, S and C_{out} in your answer.

[4]

(b).

(i) Write a Boolean expression equivalent to S.

$S \equiv$

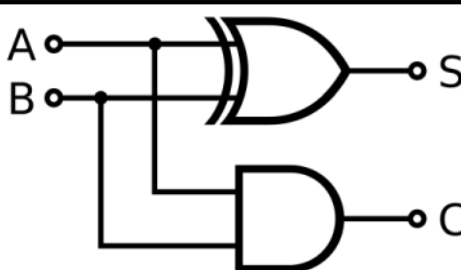
[1]

(ii) Write a Boolean expression equivalent to C_{out} .

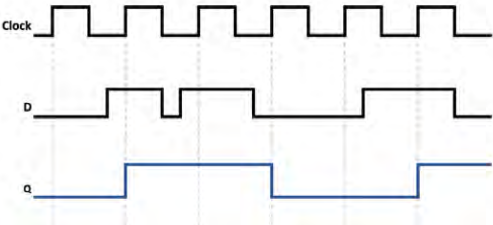
$C_{out} \equiv$

[2]

END OF QUESTION PAPER

Question		Answer/Indicative content	Marks	Guidance
1		 <p>XOR Gate (1)</p> <p>AND Gate (1)</p> <p>Correct connections and no additional gates (1)</p>	3 (AO1,1)	<p>Examiner's Comments Most candidates scored well on these questions demonstrating their understanding of logic gate circuits. Some candidates simplified the circuit in part b) which achieved full marks provided the resultant circuit gave the same output.</p>
		Total	3	
2	a	$Q = (A \wedge B) \vee (C \wedge D)$ <p>1 mark for $(A \wedge B)$</p> <p>1 mark for $(C \wedge D)$</p> <p>1 mark for the \vee joining the two parts.</p>	3 (AO1.2)	<p>Accept $(C \wedge D) \vee (A \wedge B)$</p> <p>Accept $(B \wedge A)$ instead of $(A \wedge B)$</p> <p>Accept $(D \wedge C)$ instead of $(C \wedge D)$</p> <p>Accept alternative notations (e.g. +/ OR / AND)</p> <p>Accept AB as $(A.B)$ and CD as $(C.D)$</p> <p>Accept answers without brackets</p> <p>Examiner's Comments In general, most candidates achieved all of the available marks in these questions.</p>

Question			Answer/Indicative content						Marks	Guidance
	b	i	E	F	G	$(E \wedge F)$	$(E \wedge G)$	$(E \wedge F) \vee (E \wedge G)$	4 (AO1.2)	
			1	1	1	1	1	1		
			1	1	0	1	0	1		
			1	0	1	0	1	1		
			1	0	0	0	0	0		
			0	1	1	0	0	0		
			0	1	0	0	0	0		
			0	0	1	0	0	0		
			0	0	0	0	0	0		
			1 mark for each of the pairs of rows.							
		ii	$(F \vee G) \wedge E$						2 (AO2.2)	Accept: $(G \vee F) \wedge E$ $E \wedge (F \vee G)$ $E \wedge (G \vee F)$
			One mark for the $(F \vee G)$							Examiner's Comments In general, most candidates achieved all of the available marks in these questions.
			One mark for the $\wedge E$							
			Total						9	

Question			Answer/Indicative content	Marks	Guidance																																																																																					
3		i	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>C</th> <th>D</th> <th>Output</th> </tr> </thead> <tbody> <tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr> </tbody> </table>	A	B	C	D	Output	1	1	1	1	1	1	1	1	0	1	1	1	0	1	1	1	1	0	0	1	1	0	1	1	1	1	0	1	0	1	1	0	0	1	1	1	0	0	0	1	0	1	1	1	1	0	1	1	0	1	0	1	0	1	1	0	1	0	0	1	0	0	1	1	1	0	0	1	0	1	0	0	0	1	1	0	0	0	0	0	4 AO2.2	
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		ii	$(A \vee B) \vee (C \vee D) \equiv \text{Output}$ $A \vee B$ (1 Mark) $\vee (C \vee D)$ (1 Mark)	2 AO2.2	<p>Accept answer without brackets.</p> <p>Accept alternative notation i.e. OR, +</p> <p>Examiner's Comment Boolean expressions were in the main correct. All standard notations was credited provided it was used consistently.</p>																																																																																					
			Total	6																																																																																						
4	a		To store the state of a bit	1																																																																																						
	b		 <p>One mark for each two correct clock cycles.</p>	3																																																																																						
			Total	4																																																																																						

Question			Answer/Indicative content	Marks	Guidance
5		i	A	1	
		ii	$\neg(A \vee B)$	1	
		iii	$A \vee B$	1	
			Total	3	

Question			Answer/Indicative content	Marks	Guidance																																													
6	a	i	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>A</th> <th>B</th> <th>C_{in}</th> <th>S</th> <th>C_{out}</th> </tr> </thead> <tbody> <tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr> </tbody> </table> <p>1 Mark for rows 1 and 2</p> <p>1 Mark for rows 3 and 4</p> <p>1 Mark for rows 5 and 6</p> <p>1 Mark for rows 7 and 8</p>	A	B	C _{in}	S	C _{out}	1	1	1	1	1	1	1	0	0	1	1	0	1	0	1	1	0	0	1	0	0	1	1	0	1	0	1	0	1	0	0	0	1	1	0	0	0	0	0	0	4	
A	B	C _{in}	S	C _{out}																																														
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0	0	0	0	0																																														
		ii	<ul style="list-style-type: none"> - Circuit adds two bits (and a carry bit) together / is an adder. - A B and C_{in} are added together - The result is given in S - And a carry bit in C_{out} (1 per -)	4																																														
	b	i	$S \equiv A \vee B \vee C_{in}$	1	Accept XOR instead of \vee Accept \oplus instead of \vee																																													
		ii	$C_{out} \equiv ((A \vee B) \wedge C_{in}) \vee (A \wedge B)$ One mark for $((A \vee B) \wedge C_{in})$ One mark for $\vee (A \wedge B)$	2	Accept XOR instead of \vee Accept \oplus instead of \vee Accept AND instead of \wedge Accept OR instead of \vee Accept + instead of \vee																																													
Total				11																																														