

## **Cambridge International Examinations**

Cambridge International Advanced Subsidiary and Advanced Level

CANDIDATE NAME				
CENTRE NUMBER		CANDIDATE NUMBER		

# 6054198968

**COMPUTER SCIENCE** 

9608/12

Paper 1 Theory Fundamentals

October/November 2018

1 hour 30 minutes

Candidates answer on the Question Paper.

No Additional Materials are required.

No calculators allowed.

### **READ THESE INSTRUCTIONS FIRST**

Write your Centre number, candidate number and name in the spaces at the top of this page. Write in dark blue or black pen.

You may use an HB pencil for any diagrams, graphs or rough working.

Do not use staples, paper clips, glue or correction fluid.

DO NOT WRITE IN ANY BARCODES.

Answer all questions.

No marks will be awarded for using brand names of software packages or hardware.

At the end of the examination, fasten all your work securely together.

The number of marks is given in brackets [ ] at the end of each question or part question.

The maximum number of marks is 75.



1	A company	ic	designing	а	wehsite
	A company	15	uesigning	а	WEDSILE:

(a) The company creates a 4-colour bitmap image for the website as shown.

Each colour is represented by a letter, for example, G = grey, K = black.

G	R	G	К	W	R
G	R	G	K	W	R
G	R	G	K	W	R
G	R	G	K	W	R
G	G	G	K	К	R
W	W	W	W	К	R

	(i)	State the minimum number of bits needed to represent each pixel in the image in <b>part (a</b> )	).
		[1	]
	(ii)	Calculate the minimum file size of the image shown in <b>part (a)</b> . Show your working.	
		Working	
		File size[3	 R1
(b)		company takes a photograph of their office to put on the website. The photograph ha solution of 1000 pixels by 1000 pixels. Two bytes per pixel are used to represent the urs.	
	(i)	Estimate the file size of the photograph in megabytes. Show your working.	
		Working	
		Estimated file size	

**Compression type** 

(ii) The file size of the photograph needs to be reduced before it is placed on	the website
---	-------------

Method

Draw lines to link each method of reducing the file size of the image to:

its description and

**Description** 

• its compression type, where appropriate.

Removes pixels	Crop the	
Reduces number of	Crop the photograph	Lossy
pixels per inch	Use run-length encoding	
Uses fewer bits per pixel	Use fewer	Lossless
Stores colour code and	colours	
count of repetitions		[5]
(c) The company has created	l a logo for the website. The logo is a vect	
Describe <b>two</b> reasons why	y a vector graphic is a sensible choice for	the logo.
Reason 1		
Reason 2		
		[4]

[3]

2

Go	pal typ	bes the Uniform Resource Locator (URL) of a website into a web browser.	
(a)		following sequence (1 to 5) describes the steps that take place. There are three missi ements.	ng
	1	Gopal types into the web browser.	
	2		
	3	DNS looks up the URL in table	
	4		
	5		
	Thre	e statements <b>A</b> , <b>B</b> and <b>C</b> are used to complete the sequence.	
	A	DNS finds corresponding IP address	
	В	Web browser sends URL to Domain Name Service (DNS)	
	С	DNS returns IP address to web browser	
			[2]
(c)	A tel	ecommunications operator has installed fibre-optic cables in Gopal's neighbourhood.	
	(i)	Give three benefits of fibre-optic cable over copper cable.	
		4	
		1	
		1	
		2	

(ii)	Give <b>two</b> drawbacks of fibre-optic cable over copper cable.
	1
	2
	[2]

3 The following table shows assembly language instructions for a processor which has one general purpose register, the Accumulator (ACC) and an Index Register (IX).

Ins	truction	Funlanation
Op code	Operand	Explanation
LDD	<address></address>	Direct addressing. Load the contents of the location at the given address to ACC.
LDX	<address></address>	Indexed addressing. Form the address from <address> + the contents of the Index Register. Copy the contents of this calculated address to ACC.</address>
LDR	#n	Immediate addressing. Load the number n to IX.
STO	<address></address>	Store contents of ACC at the given address.
ADD	<address></address>	Add the contents of the given address to ACC.
INC	<register></register>	Add 1 to the contents of the register (ACC or IX).
DEC	<register></register>	Subtract 1 from the contents of the register (ACC or IX).
CMP	<address></address>	Compare contents of ACC with contents of <address>.</address>
JPE	<address></address>	Following compare instruction, jump to <address> if the compare was True.</address>
JPN	<address></address>	Following compare instruction, jump to <address> if the compare was False.</address>
JMP	<address></address>	Jump to the given address.
OUT		Output to the screen the character whose ASCII value is stored in ACC.
END		Return control to the operating system.

(a) (i)	State what is meant by <b>absolute addressing</b> and <b>symbolic addressing</b> .
	Absolute addressing
	Symbolic addressing
	[2]
(ii)	Give an example of an ${\tt ADD}$ instruction using both absolute addressing and symbolic addressing.
	Absolute addressing
	Symbolic addressing
	[2]

(b)	(i)	State what is meant by <b>indexed addressing</b> and <b>immediate addressing</b> .	
		Indexed addressing	
		Immediate addressing	
			[2]
	(ii)	Give an example of an instruction that uses:	
		Indexed addressing	
		Immediate addressing	[2]
(c)	The	current contents of a general purpose register (X) are:	
		x 1 1 0 0 0 0 1	
	(i)	The contents of X represent an unsigned binary integer.	
		Convert the value in X into denary.	
			[1]
	(ii)	The contents of X represent an unsigned binary integer.	
		Convert the value in X into hexadecimal.	
			[1]
	(iii)	The contents of X represent a two's complement binary integer.	
		Convert the value in X into denary.	
			[1]

(d) The current contents of the main memory, Index Register (IX) and selected values from the ASCII character set are:

### **Address** Instruction LDD 100 40 41 CMP 104 42 JPE 54 43 LDX 100 44 CMP 105 45 JPN 47 OUT 46 LDD 100 47 48 DEC ACC 49 STO 100 50 INC IX 51 JMP 41 52 53 54 END ... 2 100 302 101 303 102 103 303 104 0 303 105

|--|

ASCII code	Character
300	/
301	*
302	-
303	+
304	٨
305	=

IX 1

This is a copy of the instruction set.

Instruction Op code Operand		Explanation			
LDX	<address></address>	Indexed addressing. Form the address from <address> + the contents of the Index Register. Copy the contents of this calculated address to ACC.</address>			
LDR	#n	Immediate addressing. Load the number n to IX.			
STO	<address></address>	Store contents of ACC at the given address.			
ADD <address></address>		Add the contents of the given address to ACC.			
INC	<register></register>	Add 1 to the contents of the register (ACC or IX).			
DEC	<register></register>	Subtract 1 from the contents of the register (ACC or IX).			
CMP	<address></address>	Compare contents of ACC with contents of <address>.</address>			
JPE	<address></address>	Following a compare instruction, jump to <address> if the compare was True.</address>			
JPN	<address></address>	Following a compare instruction, jump to <address> if the compare was False.</address>			
JMP <address></address>		Jump to the given address.			
OUT		Output to the screen the character whose ASCII value is stored in ACC.			
END		Return control to the operating system.			

Complete the trace table for the given assembly language program.

Instruction	Memory address					IV	OUTPUT		
address	ACC	100	101	102	103	104	105	- IX	OUTPUT
		2	302	303	303	0	303	1	
40									

			os of the fetch stage of the fetch-execute (FE) cycle in register tran ade some errors.
Li	ne 1	$MDR \leftarrow$	[PC]
Li	ne 2	PC ← F	PC + 1
Li	ne 3	$MDR \leftarrow$	[MAR]
Li	ne 4	$CIR \leftarrow$	PC
	entify the line each error		s of <b>three</b> errors that the student has made. Write the correct not
Line	number o	of error	Correct notation
(b) On	Give thre	ee differen	cle includes checking for interrupts. t events that can generate an interrupt.
	Give <b>thre</b> 1	ee differen	events that can generate an interrupt.
	Give <b>thre</b> 1	ee differen	events that can generate an interrupt.
	Give three  1	ee differen	events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.
(i)	Give three  1	ee differen	t events that can generate an interrupt.

[2]

1	<b>C</b>	The	processor	LISAS	huses	in	the	FF	cycle	
l	L,	, ,,,,,	processor	u5 <del>c</del> 5	nuses	ш	uie	$\Gamma$	Cycle	

The diagram shows three buses and two descriptions.

Draw **one** line from each bus to its appropriate description.

Bus	Description
Control bus	Unidirectional
	(one direction)
Address bus	
	Bidirectional (two directions)
Data bus	

- 5 This question presents three scenarios. For each scenario, tick (✓) **one** box to show whether you think the person's behaviour is ethical or unethical. Justify your choice.
  - (a) Wendy is a software engineer who is developing a program for her company. Her friend, Noah, is developing a program for a different company. Wendy looks at the code that Noah is writing to get ideas for her own program.

	Ethical						
	Unethical						
Justi	fication						
ouou							•••
•••••							
			 •••••		• • • • • • • • • • • • • • • • • • • •		

(b) Amit is fixing some bugs in the computer system of a large multinational company. He is asked to sign a confidentiality agreement. He sees some confidential information which

		contains the names of other multinational companies that have broken the law. He copies this information and releases it on the Internet.
		Ethical Unethical
		Justification
		[2]
	(c)	Farah is providing a company with an estimate for the cost of writing a program. The company she works for is in financial difficulty so she increases the estimate by 10%.            Ethical           Unethical
		Justification
		[2]
6	Kim	is using her laptop computer to write a program in a high-level language.
	(a)	Kim needs to make sure the program is secure against unauthorised access. She has already set up a username and password on her laptop.
		Identify <b>two</b> additional electronic measures that Kim can use to keep the program secure.
		1
		2[2]
	(b)	Kim will use library routines in her program.
		(i) Describe what is meant by a library routine.
		[2]

	(ii)	Describe <b>one</b> benefit and <b>one</b> drawback of using library routines.						
		Benefit						
		Drawback						
			[4]					
(c)		develops her program and makes it ready for use. To do this, she uses first an interprethen a compiler.	eter					
	Ехр	lain why Kim needs to use both an interpreter and a compiler.						
	Inte	rpreter						
	Cor	npiler						
			 [4]					

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